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/Benjamin L. Volk, Jr./

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Reg. No. 48,017

In re application of: Chamberlain et al.	:	
	:	
Serial No.: 10/550,323	:	Examiner: Fleurantin, Jean B.
	:	
Filed: September 1, 2006	:	Group Art Unit: 2162
	:	
For: Intelligent Data Storage and	:	
Processing Using FPGA Devices	:	

**LETTER TO PTO**

Applicant electronically filed an Amendment After Final and an IDS with references on February 23, 2011. We have today re-submitted three references after observing that the version in PAIR contained distorted text.

Favorable action is respectfully requested.

Respectfully submitted,

**Thompson Coburn LLP**

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